



Form PTO-1449 (modified)
List of Patents and Publication
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)

ATTY. DKT. NO. 5500-78100

SERIAL NO. 09/927,054

APPLICANT: McGrath

GROUP: 2183

FILING DATE: August 9, 2001

U.S. PATENT DOCUMENTS

	A9	5,684,993	11/4/97	Willman		
	A18	5,418,956	5/23/95	Willman		

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

RUE	A1	<u>The Technology Behind Crusoe™ Processors, Low-Power x86-Compatible Processors Implemented with Code Morphing™ Software</u> , Transmeta Corporation, January 2000, pages 1-18.
RUE	A2	<u>Alpha Learns to Do Windows; Digital's FX!32 is The Key To Running Win32 Software on Alpha/Windows NT.</u> , Selinda Chiquoine, BYTE, August 4, 1998, 4 pages.
RUE	A3	<u>Awards Stack Up for DIGITAL FX!32 Windows Compatibility Software for ALPHA</u> , DIGITAL Press Releases, December 11, 1997, 7 pages.
RUE	A4	<u>DIGITAL FX!32; White Paper: How DIGITAL FX!32 Works</u> , DIGITAL Semiconductor, January 26, 1998, 4 pages.
RUE	A5	<u>An Alpha in PC Clothing; Digital Equipment's New x86 Emulator Technology Makes An Alpha System a Fast x86 Clone</u> , Tom Thompson, BYTE, August 4, 1998, 7 pages.
RUE	A6	<u>AMD 64-Bit Technology; The AMD x86 Architecture Programmers Overview</u> , AMD, Publication #24108 Rev: A, August 2000, pages 1-106.
RUE	A7	<u>AMD 64-Bit Technology; The AMD x86-64 Architecture Programmers Overview</u> , AMD, Publication #24108 Rev: C, January 2001, pages 1-128.
RUE	A8	<u>Alpha Architecture Reference Manual</u> , Third Edition, Digital Press, 1998 Digital Equipment Corporation, pages 5-14 to 5-15, 5-18 to 5-19, 5-30 to 5-35.
RUE	A10	<u>32-Bit Microprocessor User's Manual</u> , MC68020, Third Edition, Motorola, 1989, pp. 3-105 and 3-184..
RUE	A11	<u>Intel Architecture Software Developer's Manual</u> , Volume 1: Basic Architecture, 1997, pages 6-17 to 6-19.
RUE	A12	Intel, "Intel Architecture Software Developer's Manual, Vol. 2: Instruction Set Reference," 1997, pages 3-29, 3-66 to 3-69, 3-196 to 3-197, 3-460 to 3-463..
RUE	A13	AMD, "SYSCALL and SYSRET Instruction Specification", Application Note, 1998, pages 1-10.
RUE	A14	Intel, "Intel Architecture Software Developer's Manual, Vol. 2: Instruction Set Reference," 1997, pages 3-256 to 3-258, 3-286 to 3-290, 3-350 to 3-353, 3-385 to 3-387.
RUE	A15	<u>Intel Architecture Software Developer's Manual</u> , Volume 1: Basic Architecture, 1997, pages 3-38 to 3-39.
RUE	A16	<u>Addendum-Intel Architecture Software Developer's Manual</u> , Vol. 2: Instruction Set Reference, 1996/1997, Chapter 3, pages 3-1 to 3-15.
RUE	A17	<u>Pentium Pro Family Developer's Manual</u> , Volume 3: Operating System Writer's Guide, 1996, pages 2-1 to 2-21, 3-1 to 3-33, 4-1 to 4-29.

EXAMINER:

Richard Ellis

DATE CONSIDERED:

9/23/2004

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.

Information Disclosure Statement--PTO 1449 (modified)